

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10714271
<b>Filing Date:</b>	13-Nov-2003
<b>Title of Invention:</b>	METHOD OF FABRICATING A LATERAL DOUBLE-DIFFUSED MOSFET (LDMOS) TRANSISTOR AND A CONVENTIONAL CMOS TRANSISTOR
<b>First Named Inventor/Applicant Name:</b>	Budong You
<b>Filer:</b>	David J. Goren/Mae Pattison
<b>Attorney Docket Number:</b>	09464-026001

Filed as Small Entity

### **Utility Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
-------------	----------	----------	--------	----------------------

**Basic Filing:**

**Pages:**

**Claims:**

**Miscellaneous-Filing:**

**Petition:**

**Patent-Appeals-and-Interference:**

**Post-Allowance-and-Post-Issuance:**

Utility Appl issue fee	2501	1	700	700
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Extension-of-Time:</b>				
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>1000</b>